

# TFT DISPLAY SPECIFICATION



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## SPECIFICATION

**MODULE NO.: WF0340ATYAB7MNN0**

### General Specifications

| Item             | Dimension                         | Unit |
|------------------|-----------------------------------|------|
| Size             | 3.4                               | inch |
| Dot Matrix       | 800 x RGB x 800 (TFT)             | dots |
| Module dimension | 94.9 (W) x 96.95 (H) x 2.22 (D)   | mm   |
| Active area      | 87.6 x 87.6                       | mm   |
| Pixel pitch      | 0.1095 x 0.1095                   | mm   |
| LCD type         | TFT, Normally black, Transmissive |      |
| Viewing Angle    | 85/85/85/85                       |      |
| IC               | ILI9881C or equivalent            |      |
| TFT Interface    | 4-Lanes MIPI                      |      |
| Backlight Type   | LED, Normally White               |      |
| With /Without TP | Without TP                        |      |
| Surface          | Anti-Glare                        |      |

\*Color tone slight changed by temperature and driving voltage.



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# Absolute Maximum Ratings

| Item                       | Symbol    | Min  | Typ | Max       | Unit |
|----------------------------|-----------|------|-----|-----------|------|
| Operating Temperature      | TOP       | -20  | —   | +70       | °C   |
| Storage Temperature        | TST       | -30  | —   | +80       | °C   |
| Power Supply voltage 1     | VCI~GND   | -0.3 | —   | +3.6      | V    |
| Power Supply voltage 2     | IOVCC~GND | -0.3 | —   | +3.3      | V    |
| Logic Input Voltage Range  | VIN       | -0.3 | —   | IOVCC+0.3 | V    |
| Logic Output Voltage Range | VO        | -0.3 | —   | IOVCC+0.3 | V    |

\*The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.



# Electrical Characteristics

Operating conditions:

| Item                              | Symbol  | Min       | Typ | Max       | Unit |
|-----------------------------------|---------|-----------|-----|-----------|------|
| Supply voltage for analog circuit | VCI     | 2.5       | 3.3 | 3.6       | V    |
| Supply voltage for logic circuit  | IOVCC   | 1.65      | 1.8 | 3.3       | V    |
| Current for VCI                   | I-vci   | -         | 20  | 30        | mA   |
| Current for IOVCC                 | I-iovcc | -         | 20  | 30        | mA   |
| Input voltage "H" level           | VIH     | 0.7*IOVCC | —   | IOVCC     | V    |
| Input voltage "L" level           | VIL     | GND       | —   | 0.3*IOVCC | V    |
| Output voltage "H" level          | VOH     | 0.8*IOVCC | —   | IOVCC     | V    |
| Output voltage "L" level          | VOL     | GND       | —   | 0.2*IOVCC | V    |

## Interface

LCM PIN Definition

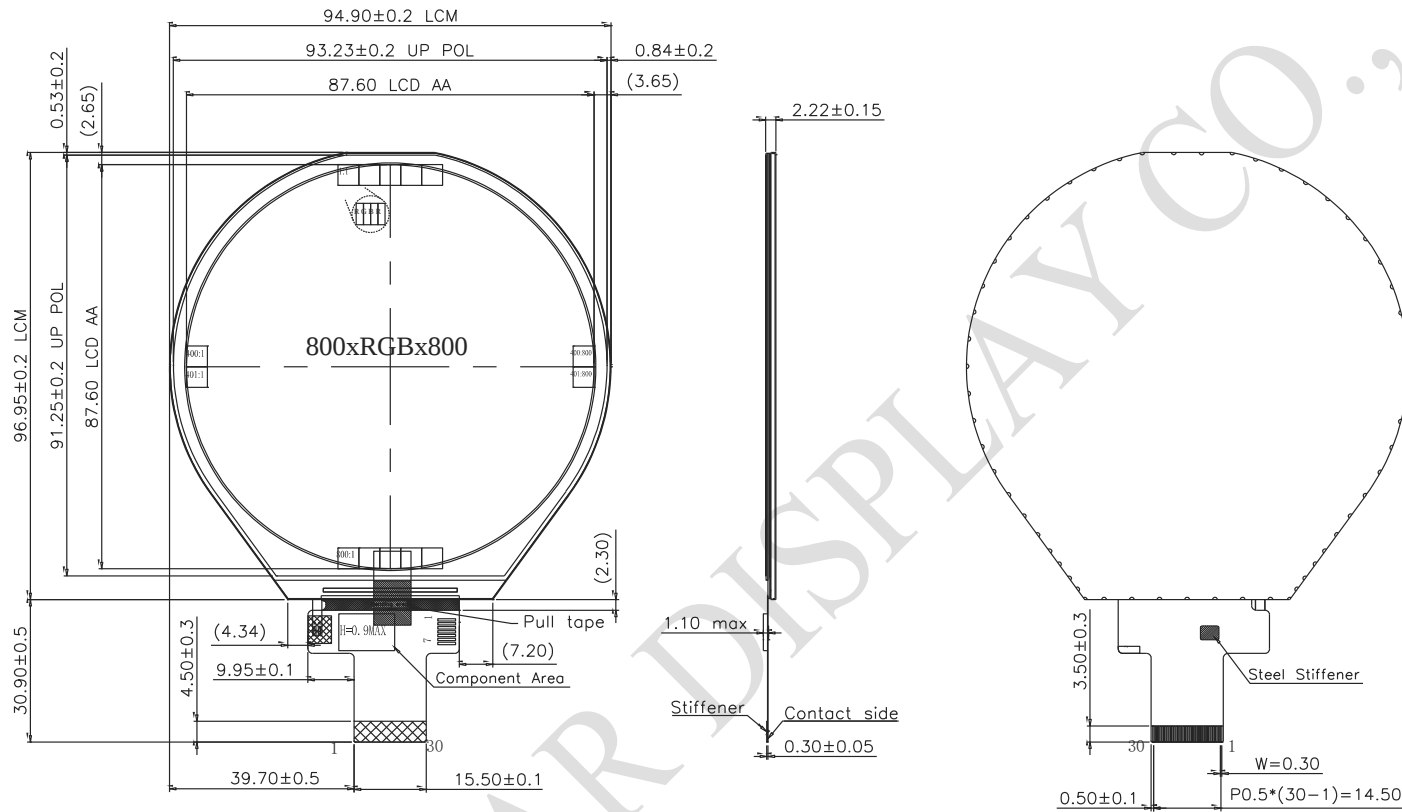
| Pin | Symbol | Function                                                        | Remark |
|-----|--------|-----------------------------------------------------------------|--------|
| 1   | VLED+  | VLED backlight anode.                                           |        |
| 2   | VLED-  | VLED backlight cathode.                                         |        |
| 3   | VLED-  | VLED backlight cathode.                                         |        |
| 4   | VCI    | Power supply for the analog power                               |        |
| 5   | IOVCC  | Power supply for the logic power and I/O circuit (IOVCC = VDDI) |        |
| 6   | RESET  | Reset signal(low active) (RESET=RESX)                           |        |
| 7   | TE     | Tearing effect output                                           |        |
| 8   | NC     | No connection                                                   |        |
| 9   | GND    | Power ground                                                    |        |
| 10  | D0P    | MIPI-DSI data lane 0 positive input pin.                        |        |
| 11  | D0N    | MIPI-DSI data lane 0 negative input pin                         |        |
| 12  | GND    | Power ground                                                    |        |
| 13  | D1P    | MIPI-DSI data lane 1 positive input pin.                        |        |
| 14  | D1N    | MIPI-DSI data lane 1 negative input pin.                        |        |
| 15  | GND    | Power ground                                                    |        |



|       |      |                                          |  |
|-------|------|------------------------------------------|--|
| 16    | CLKP | MIPI-DSI data lane positive input pin.   |  |
| 17    | CLKN | MIPI-DSI data lane negative input pin.   |  |
| 18    | GND  | Power ground                             |  |
| 19    | D2P  | MIPI-DSI data lane 2 positive input pin. |  |
| 20    | D2N  | MIPI-DSI data lane 2 negative input pin  |  |
| 21    | GND  | Power ground                             |  |
| 22    | D3P  | MIPI-DSI data lane 3 positive input pin. |  |
| 23    | D3N  | MIPI-DSI data lane 3 negative input pin  |  |
| 24    | GND  | Power ground                             |  |
| 25-30 | NC   | No connection                            |  |



# Contour Drawing



| PIN: | SYMBLE |
|------|--------|
| 1    | VLED+  |
| 2    | VLED-  |
| 3    | VLED-  |
| 4    | VCI    |
| 5    | IOVCC  |
| 6    | RESET  |
| 7    | TE     |
| 8    | NC     |
| 9    | GND    |
| 10   | DOP    |
| 11   | DON    |
| 12   | GND    |
| 13   | D1P    |
| 14   | D1N    |
| 15   | GND    |
| 16   | CLKP   |
| 17   | CLKN   |
| 18   | GND    |
| 19   | D2P    |
| 20   | D2N    |
| 21   | GND    |
| 22   | D3P    |
| 23   | D3N    |
| 24   | GND    |
| 25   | NC     |
| 26   | NC     |
| 27   | NC     |
| 28   | NC     |
| 29   | NC     |
| 30   | NC     |

The non-specified tolerance of dimension is ±0.3 mm .