

# 150 WATT QSW DC/DC CONVERTERS



## Description

The 4:1 Input Voltage 150 Watt Single QSW DC/DC converter provides a precisely regulated dc output. The output voltage is fully isolated from the input, allowing the output to be positive or negative polarity and with various ground connections. The 150 Watt QSW meets the most rigorous performance standards in an industry standard footprint for mobile ( $12V_{IN}$ ), process control ( $24V_{IN}$ ), and military COTS ( $28V_{IN}$ ) applications.

The 4:1 Input Voltage 150 Watt QSW includes remote ON/OFF. Threaded through holes are provided to allow easy mounting or addition of a heatsink for extended temperature operation.

The converters high efficiency and high power density are accomplished through use of high-efficiency synchronous rectification technology, advanced electronic circuit, packaging and thermal design thus resulting in a high reliability product. Converter operates at a fixed frequency and follows conservative component de-rating guidelines.

Product is designed and manufactured in the USA.

## Features

- 4:1 Input voltage range
- High power density
- Small size 1.54" x 2.39" x 0.52"
- Efficiency up to 94%
- Excellent thermal performance with metal case
- Over-Current and Short Circuit Protection
- Over-Temperature protection
- Auto-restart
- Monotonic startup
- Constant frequency
- Remote ON/OFF
- Good shock and vibration damping
- RoHS Compliant

Model	Input Range VDC		Vout VDC	Iout ADC
	Min	Max		
24S12.12QSW (ROHS)	9	36	12	12

**1. Designed to meet MIL-STD-810G for functional shock and vibration. The unit must be properly secured to the interface medium (PCB/Chassis) by use of the threaded inserts of the unit.**

**2. A thermal management device, such as a heatsink, is required to ensure proper operation of this device. The thermal management medium is required to maintain baseplate < 100°C for full rated power.**

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## Electrical Specifications:

Conditions:  $T_A = 25^{\circ}\text{C}$ , airflow = 300 LFM (1.5m/s),  $V_{IN} = 24\text{VDC}$ , unless otherwise specified. Specifications subject to change without notice.

24S12.12QSW					
Parameter	Notes	Min	Typ	Max	Units
<b>Absolute Maximum Ratings</b>					
Input Voltage	Continuous	0		40	V
	Transient (100ms)			50	V
Operating Temperature	Baseplate (100% load)	-40		100	$^{\circ}\text{C}$
Storage Temperature		-55		125	$^{\circ}\text{C}$
<b>Isolation Characteristics and Safety</b>					
Isolation Voltage	Input to Output	2250			V
	Input to Baseplate & Output to Baseplate	1500			V
Isolation Capacitance			4500		pF
Isolation Resistance		10	20		M $\Omega$
Insulation Safety Rating			Basic		
Designed to meet UL/cUL 60950, IEC/EN 60950-1					
<b>Feature Characteristics</b>					
Fixed Switching Frequency			285		kHz
Remote Sense Compensation	This function is not provided		N/A		%
Output Overvoltage Protection	Non-latching	110	115	120	%
Over Temperature Shutdown (Baseplate)	Non-latching		110	115	$^{\circ}\text{C}$
Auto-Restart Period	Applies to all protection features	450	500	550	ms
Turn-On Time from $V_{IN}$	Time from UVLO to $V_O=90\% V_{OUT}$ (NOM)	400	500	600	ms
Turn-On time from ON/OFF Control	Time from ON to $V_O=90\% V_{OUT}$ (NOM) Resistive load	13	17	21	ms
Rise Time	$V_{OUT}$ from 10% to 90%	3	5.5	7	ms
<b>ON/OFF Control – Positive Logic</b>					
On State	Pin open = ON or external voltage applied	2		5.5	V
Current Control	Leakage current			0.16	mA
OFF State		0		0.8	V
Control Current	Sinking			0.36	mA
<b>Thermal Characteristics</b>					
Thermal resistance Baseplate to Ambient	Unit mounted horizontally, using Calex Brick Test Fixture (bottom side of fixture covered to prevent airflow across unit), No Heatsink & Fan used.		6.2		$^{\circ}\text{C/W}$



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## Electrical Specifications (Continued):

Conditions:  $T_A = 25^\circ\text{C}$ , airflow = 300 LFM (1.5m/s),  $V_{IN} = 24\text{VDC}$ , unless otherwise specified. Specifications subject to change without notice.

24S12.12QSW					
Parameter	Notes	Min	Typ	Max	Units
<b>Input Characteristics</b>					
Operating Input Voltage Range		9	24	36	V
Input Under Voltage Lockout	Non-latching				
Turn-on Threshold		8.3	8.6	8.9	V
Turn-off Threshold		7.6	7.9	8.2	V
Lockout Hysteresis Voltage		0.3	0.6	0.9	V
Maximum Input Current	$V_{IN} = 9\text{V}$ , 100% Load			18	A
	$V_{IN} = 24\text{V}$ , 100% Load			7	A
	$V_{IN} = 24\text{V}$ , Output Shorted		0.05		$A_{RMS}$
Input Stand-by Current	Converter Disabled		17	20	mA
Input Current @ No Load	Converter Enabled		177		mA
Minimum Input Capacitance (external)	ESR < 0.1 $\Omega$	470			$\mu\text{F}$
Inrush Transient	$V_{in} = 36\text{V}$ (0.4V/ $\mu\text{s}$ ) no input external capacitor		0.05		$\text{A}^2\text{s}$
Input Terminal Ripple Current, $i_c$	25 MHz bandwidth, 100% Load (Fig. 2)		2.03		$A_{RMS}$
<b>Output Characteristics</b>					
Output Voltage Range		11.64	12.00	12.36	V
Output Voltage Set Point Accuracy	(50% Load)	11.88	12.00	12.12	V
Output Regulation					
Over Line	$V_{IN} = 9\text{V}$ to 36V		0.2	1	%
Over Load	$V_{IN} = 24\text{V}$ , Load 0% to 100%		0.2	1	%
Temperature Coefficient			0.02	0.03	%/ $^\circ\text{C}$
Over Voltage Protection		13.2		14.4	V
Output Ripple and Noise – 20 MHz bandwidth	(Fig. 3) 100% Load		50		mV <sub>PK-PK</sub>
			15		mV <sub>RMS</sub>
External Load Capacitance	Full Load (resistive) -40 $^\circ\text{C}$ < $T_a$ < +105 $^\circ\text{C}$	$C_{EXT}$	330	1000	$\mu\text{F}$
		ESR	10	100	m $\Omega$
Output Current Range (See Fig. B)		0		12	A
Current Limit Inception			15		A
RMS Short-Circuit Current	Non-latching, Continuous		0.7		$A_{RMS}$
<b>Dynamic Response</b>					
Load change 50% - 75% - 50%, di/dt = 1A/ $\mu\text{s}$	$C_o = 470 \mu\text{F}/15\text{m}\Omega + 1 \mu\text{F}$ ceramic		$\pm 150$		mV
Load change 50% - 100%, - 50%, di/dt = 1A/ $\mu\text{s}$	$C_o = 470 \mu\text{F}/15\text{m}\Omega + 1 \mu\text{F}$ ceramic		$\pm 250$		mV
Setting Time to 1% of $V_{OUT}$			200		$\mu\text{s}$
<b>Efficiency</b>					
100% Load	$V_{IN} = 24 \text{ V}$		93		%
	$V_{IN} = 12 \text{ V}$		93		%
50% Load	$V_{IN} = 24 \text{ V}$		92		%
	$V_{IN} = 12 \text{ V}$		94		%

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**Environmental and Mechanical Specifications:** Specifications subject to change without notice.

Parameter	Notes	Min	Typ	Max	Units
Environmental					
Operating Humidity	Non-condensing			95	%
Storage Humidity	Non-condensing			95	%
ROHS Compliance <sup>1</sup>	See Calex Website <a href="http://www.calex.com/RoHS.html">http://www.calex.com/RoHS.html</a> for the complete RoHS Compliance Statement				
Shock and Vibration	Designed to meet MIL-STD-810G for functional shock and vibration				
Water Washability	Not recommended for water wash process. Contact the factory for more information.				
Mechanical					
Weight		2.4			Ounces
		68			Grams
PCB					
Operating Temperature				130	°C
Tg		170			°C
Through Hole Pin Diameters	Pins 1 ,2 and 3	0.038	0.04	0.042	Inches
		0.965	1.016	1.067	mm
	Pins 4 and 5	0.058	0.06	0.062	Inches
		1.4732	1.524	1.575	mm
Through Hole Pin Material	All Pins	Brass Alloy TB3 or “Eco Brass”			
Through Hole Pin Finish	All pins	10μ” Gold over Nickel			
Case Dimensions		1.54 x 2.39 x 0.52			Inches
		39.116 x 60.706 x 13.21			mm
Case Material	Plastic: Vectra LCP FIT30: ½ - 16 EDM Finish				
Baseplate	Material	Aluminum			
	Flatness		0.008		Inches
			0.20		mm
Reliability					
MTBF	Telcordia SR-332, Method 1 Case 1 50% electrical stress, 40°C components	8.6			MHrs
EMI and Regulatory Compliance					
Conducted Emissions	MIL-STD-461F CE102 with external EMI filter network (see Figs, 12 and 13)				

Additional Notes:

1. the RoHS marking is as follows:

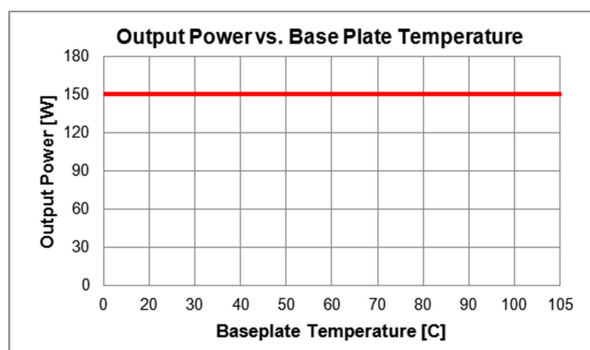


Figure A: Power derating as function of baseplate temperature

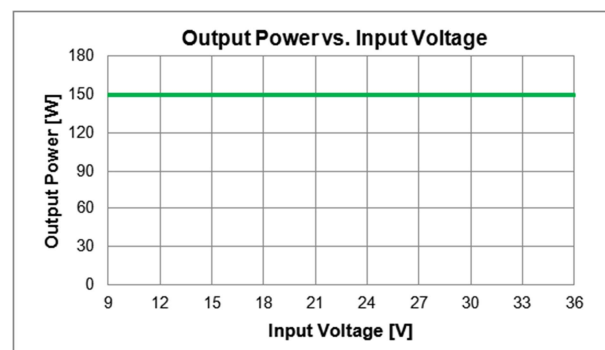


Figure B: Output Power as function of input voltage.

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## Operations:

### Input and Output Capacitance

In many applications, the inductance associated with the distribution from the power source to the input of the converter can affect the stability of the converter. This becomes of great consideration for input voltage at 12V or below. In order to enable proper operation of the converter, in particular during load transients, an additional input capacitor is required. Minimum required input capacitance, mounted close to the input pins, is 470 $\mu$ F with ESR < 0.1  $\Omega$ . Since inductance of the input power cables could have significant voltage drop due to rate of change of input current  $di(in)/dt$  during transient load operation an external capacitance on the output of the converter is required to reduce  $di(in)/dt$ . It is required to use at least 330 $\mu$ F (ESR < 0.1 $\Omega$ ) on the output. Another constraint is minimum rms current rating of the input and output capacitors which is application dependent. One component of input rms current handled by input capacitor is high frequency component at switching frequency of the converter (typ. 285kHz) and is specified under "Input terminal ripple current"  $i_c$ . Typical value at full rated load and 24  $V_{IN}$  is provided in Section "Characteristic Waveforms" and is in range of 1.5A – 2.5A. Second component of the ripple current is due to reflected step load current on the input of the converter. Similar consideration needs to be taken into account for output capacitor and in particular step load ripple current component. Consult the factory for further application guidelines.

Additionally, for EMI conducted measurement it is necessary to use 5 $\mu$ H LISNs instead of typical 50 $\mu$ H LISNs.

### ON/OFF (Pin 2)

The ON/OFF pin is used to turn the power converter on or off remotely via a system signal and has positive logic. A typical connection for remote ON/OFF function is shown in Fig. 1.

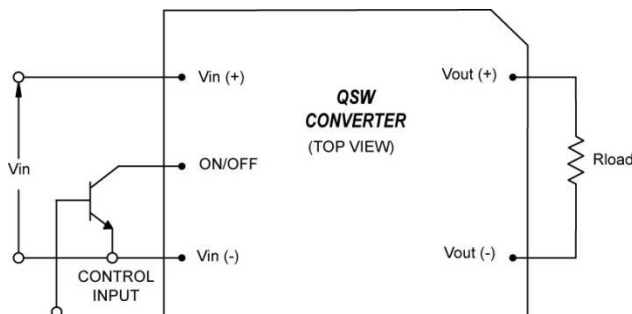


Fig 1: Circuit configuration for ON/OFF function.

The positive logic version turns on when the ON/OFF pin is at logic high and turns off when at logic low. The converter is on when the ON/OFF pin is either left open or external voltage not more than 5.5V is applied between ON/OFF pin and -INPUT pin. See the Electrical Specifications for logic high/low definitions.

The ON/OFF pin is internally pulled up to typically 4.5V via resistor and connected to internal logic circuit via RC circuit in order to filter out noise that may occur on the ON/OFF pin. A properly de-bounced mechanical switch, open-collector transistor, or FET can be used to drive the input of the ON/OFF pin. The device must be capable of sinking up to 0.36mA at a low level voltage of  $\leq 0.8V$ . During logic high, the typical maximum voltage at ON/OFF pin (generated by the converter) is 4.5V, and the maximum allowable leakage current is 160 $\mu$ A. If not using the remote on/off feature leave the ON/OFF pin open.

**TTL Logic Level** - The range between 0.81V as maximum turn off voltage and 2V as minimum turn on voltage is considered the dead-band. Operation in the dead-band is not recommended.

External voltage for ON/OFF control should not be applied when there is no input power voltage applied to the converter.

## Protection Features:

### Input Undervoltage lockout (UVLO)

Input undervoltage lockout is standard with this converter. The converter will shut down when the input voltage drops below a pre-determined voltage.

The input voltage must be typically above 8.5V for the converter to turn on. Once the converter has been turned on, it will shut off when the input voltage drops typically below 8V. If the converter is started by input voltage (ON/OFF (pin 2) is left open.

### Output Overcurrent Protection (OCP)

The converter is protected against overcurrent or short circuit conditions. At slight overload conditions cycle-by-cycle mode activates then transfers to hiccup mode at more severe overloading conditions.

Once the converter has shut down, it will attempt to restart nominally every 500msec with a typical 3% duty cycle. The attempted restart will continue indefinitely until the overload or short circuit conditions are removed. Once the output current is brought back into its specified range, the converter automatically exits the hiccup mode and continues normal operation.

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## Output Overvoltage Protection (OVP)

The converter will shut down if the output voltage across  $V_{OUT}$  (+) (Pin 5) and  $V_{OUT}$  (-) (Pin 4) exceeds the threshold of the OVP circuitry. The OVP circuitry contains its own reference, independent of the output voltage regulation loop. Once the converter has shut down, it will attempt to restart every 500 msec until the OVP condition is removed.

## Over Temperature Protection (OTP)

The QSW converter has non-latching over temperature protection. It will shut down and disable the output if temperature at the center of the base plate exceeds a threshold of 112°C (typical).

The converter will automatically restart when the base temperature has decreased by approximately 10°C.

## Safety Requirements

Basic Insulation is provided between input and the output.

The converters have no internal fuse. To comply with safety agencies requirements, a fast-acting or time-delay fuse is to be provided in the unearthed lead.

Recommended fuse values are:

- a) 20A for  $9V < V_{IN} < 18V$
- a) 9A for  $18V < V_{IN} < 36V$

## Electromagnetic Compatibility (EMC)

EMC requirements must be met at the end-product system level, as no specific standards dedicated to EMC characteristics of board mounted component dc-dc converters exist.

With the addition of a single stage external filter, the QSW converter will pass the requirements of MIL-STD-461F CE102 Base Curve for conducted emissions.

## Absence of the Remote Sense Pins and Trim Up/Down Feature

Customers should be aware that QSW converters do not have a Remote Sense feature and Output Voltage Trim Up/Down feature. Care should be taken to minimize voltage drop on the user's motherboard.

Non-standard output voltages are available. Please contact the factory for additional information.

## Thermal Consideration

The QSW converter can operate in a variety of thermal environments. However, in order to ensure reliable operation of the converter, sufficient cooling should be provided. The QSW converter is encapsulated in plastic case with metal baseplate on the top. In order to improve thermal performance, power components inside the unit are thermally coupled to the baseplate. In addition, thermal design of the converter is enhanced by use of input and out pins as heat transfer elements. Heat is removed from the converter by conduction, convection and radiation.

There are several factors such as ambient temperature, airflow, converter power dissipation, converter orientation how converter is mounted as well as the need for increased reliability that need to be taken into account in order to achieve required performance. It is highly recommended to measure temperature in the middle of the baseplate in particular application to ensure that proper cooling of the convert is provided.

A reduction in the operating temperature of the converter will result in an increased reliability.

## Thermal Derating

There are two most common applications: 1) the QSW converter is thermally attached to a cold plate inside chassis without any forced internal air circulation; 2) the QSW converter is mounted in an open chassis on system board with forced airflow with or without an additional heatsink attached to the baseplate of the QSW converter.

The best thermal results are achieved in application 1) since the converter is cooled entirely by conduction of heat from the top surface of the converter to a cold plate and temperature of the components is determined by the temperature of the cold plate. There is also some additional heat removal through the converters pins to the metal layers in the system board. It is highly recommended to solder pins to the system board rather than using receptacles. The QSW converter can deliver full power as long as temperature of the base plate of the case is kept below 100°C.



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## Soldering Guidelines

The ROHS-compliant through hole QSW converter uses Sn/Ag/Cu-b-free solder and ROHS compliant components. They are designed to be processed through wave soldering machines. The pins are 100% matte tin over nickel plated and compatible with both Pb and Pb-free wave soldering processes. It is recommended to follow specifications below when installing and soldering QSW converter. Exceeding these specifications may cause damage to the QSW converter.

Wave Solder Guideline for Sn/Ag/Cu based solders	
Maximum Preheat Temperature	115°C
Maximum Pot Temperature	270°C
Maximum Solder Dwell Time	7 seconds

Wave Solder Guideline for SN/Pb based solders	
Maximum Preheat Temperature	105°C
Maximum Pot Temperature	250°C
Maximum Solder Dwell Time	6 seconds

QSW converters are not recommended for water wash process. Contact the factory for additional information if water wash is necessary.

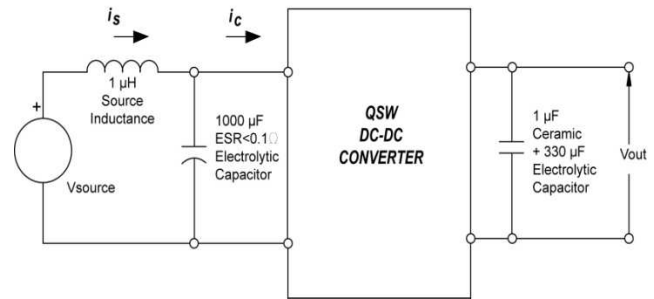


Fig. 2: Test setup for measuring input reflected ripple currents  $i_c$

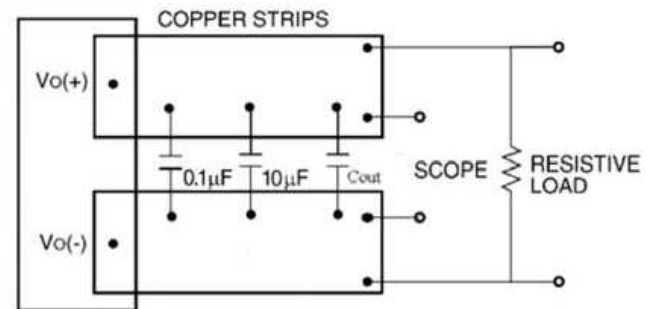


Fig. 3: Test setup for measuring output voltage ripple, startup and step load transient waveforms.

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## Characteristic Curves – Efficiency and Power Dissipation

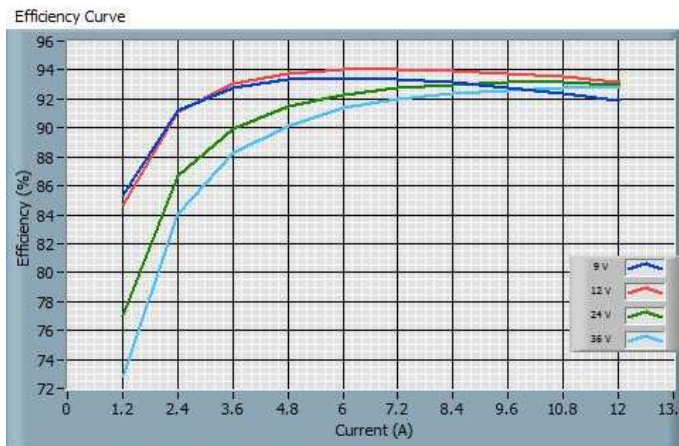


Fig. 4: 24S12.12QSW Efficiency Curve

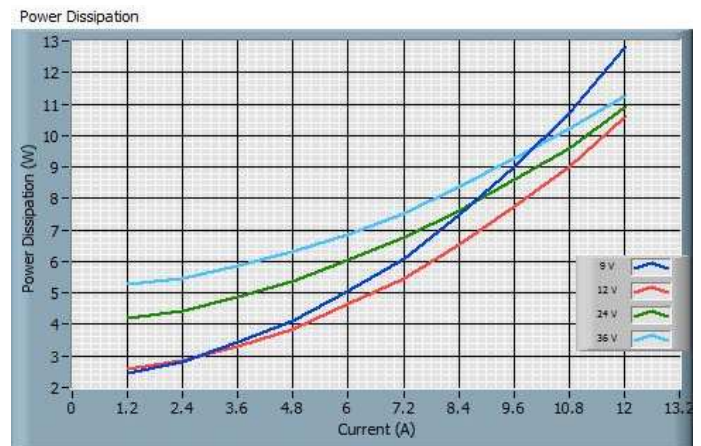


Fig. 5: 24S12.12QSW Power Dissipation



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## Characteristic Waveforms 24S12.12QSW

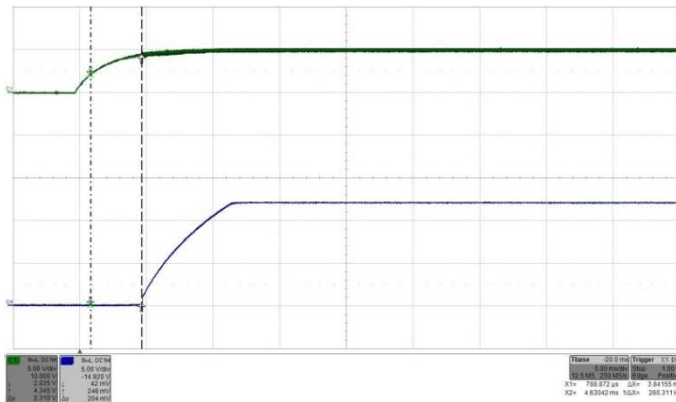


Fig. 6: Turn-on by ON/OFF transient (with  $V_{IN}$  applied) at full rated load current (resistive) at  $V_{IN} = 24V$ . Top trace (C1): ON/OFF signal (5V/div.). Bottom trace (C4): Output voltage (5 V/div.). Time 5 ms/div.

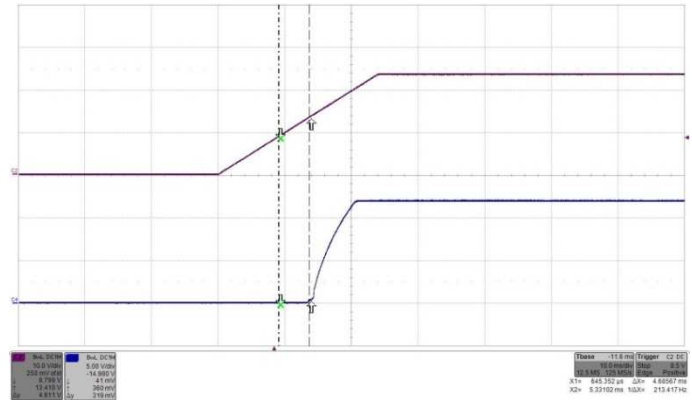


Fig. 7: Turn-on by  $V_{IN}$  transient (ON/OFF high) at full rated load current (resistive) at  $V_{IN} = 24V$ . Top trace (C2): Input voltage  $V_{IN}$  (10 V/div.). Bottom trace (C4): Output voltage (5 V/div.). Time 10 ms/div.

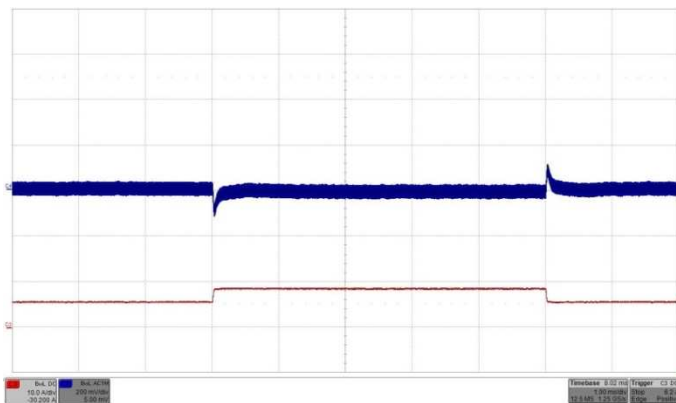


Fig. 8: Output voltage response to load current step change 50% - 75% - 50% (6A - 9A - 6A) with  $di/dt = 1A/\mu s$  at  $V_{IN} = 24V$ . Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (10A/div.).  $C_O$  330 $\mu$ F/75m $\Omega$ . Time: 1ms/div.

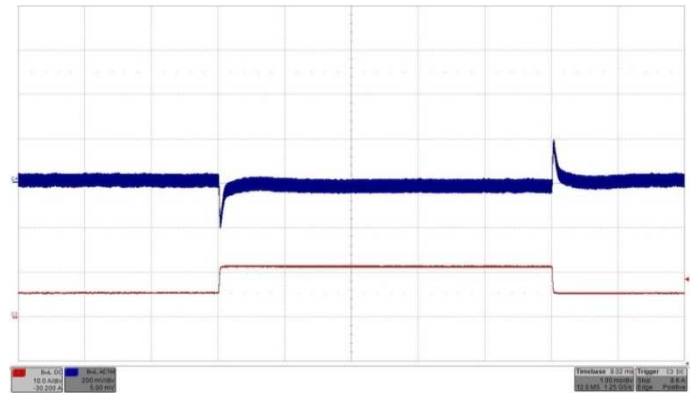


Fig. 9: Output voltage response to load current step change 50% - 100% - 50% (6A - 12A - 6A) with  $di/dt = 1A/\mu s$  at  $V_{IN} = 24V$ . Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (10A/div.).  $C_O$  330 $\mu$ F/75m $\Omega$ . Time: 1ms/div.

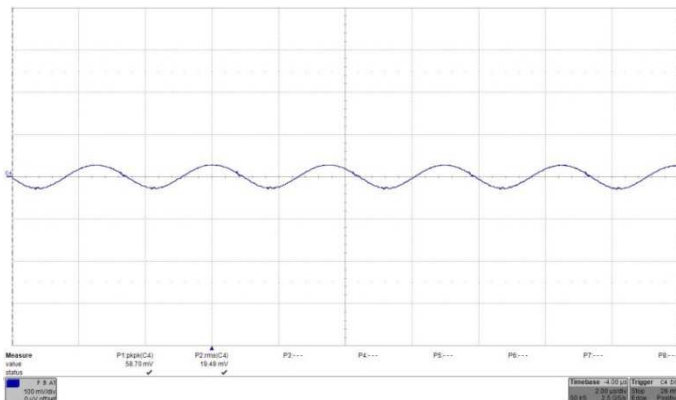


Fig. 10: Output voltage ripple (100mV/div.) at full rated load at  $V_{IN} = 24V$ .  $C_O$  330 $\mu$ F/75m $\Omega$ . Time: 2 $\mu$ s/div.

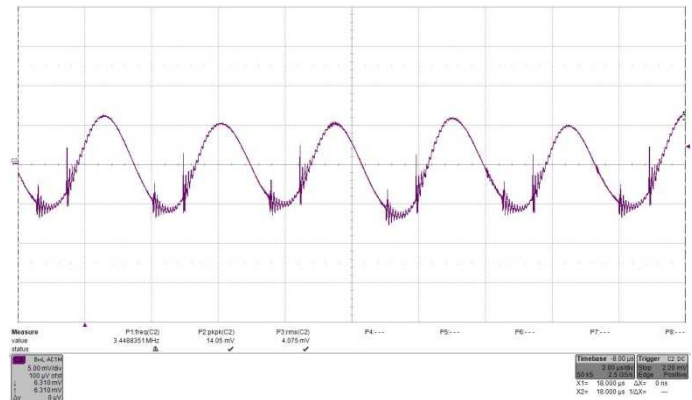


Fig. 11: Input reflected ripple current,  $i_C$  (2.5A/div), measured at input terminals at full rated load current at  $V_{IN} = 24V$ . Refer to Fig. 2 for test setup. Time: 2  $\mu$ s/div. RMS input ripple current is 2.03A.



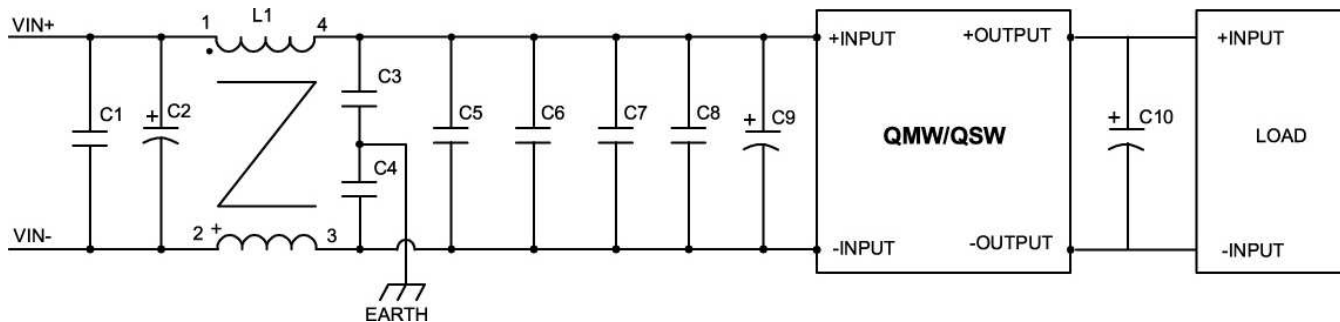
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## EMC Consideration:

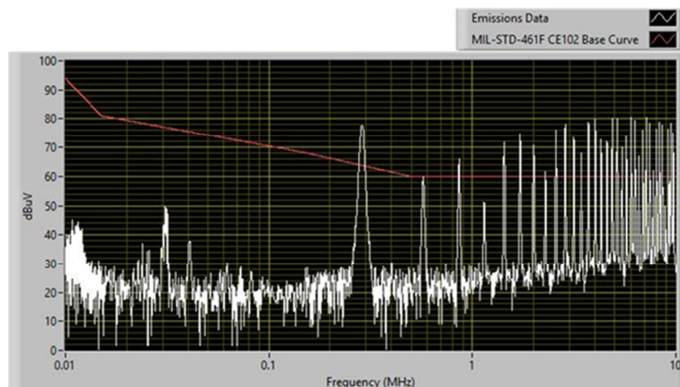
The filter schematic for suggested input filter configuration as tested to meet the conducted emission limits of MIL-STD-461F CE102 Base Curve is shown in Fig. 12. The plots of conducted EMI spectrum are shown in Fig. 13.

Note: Customer is ultimately responsible for the proper selection, component rating and verification of the suggested parts based on the end application.

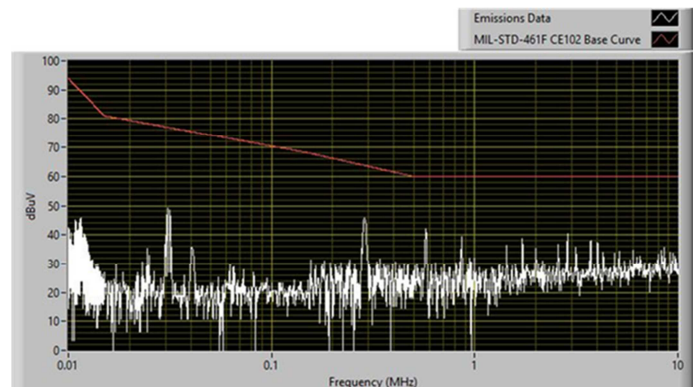


Comp. Des.	Description
C1, C5, C6, C7, C8	10 $\mu$ F/1210/X7R/50V Ceramic Capacitor
C2	220 $\mu$ F/50V Electrolytic Capacitor (Vishay MAL214699106E3 or equivalent)
C3, C4	4.7nF/1206/X7R/1500V Ceramic Capacitor
C9, C10	330 $\mu$ F/50V Electrolytic Capacitor (Vishay MAL214699107E3 or equivalent)
L1	300 $\mu$ H, CM Choke (6 turns on toroid 22.1mm x 13.7mm x 7.92mm) $L_{lk} = 1.2\mu$ H, $R_{dc} = 2m\Omega$

Fig.12: Typical input EMI filter circuit to attenuate conducted emissions per MIL-STD-461F CE102 Base Curve.



a) Without input filter. C9 = 330 $\mu$ F/50V.



b) With input filter from Fig. 12.

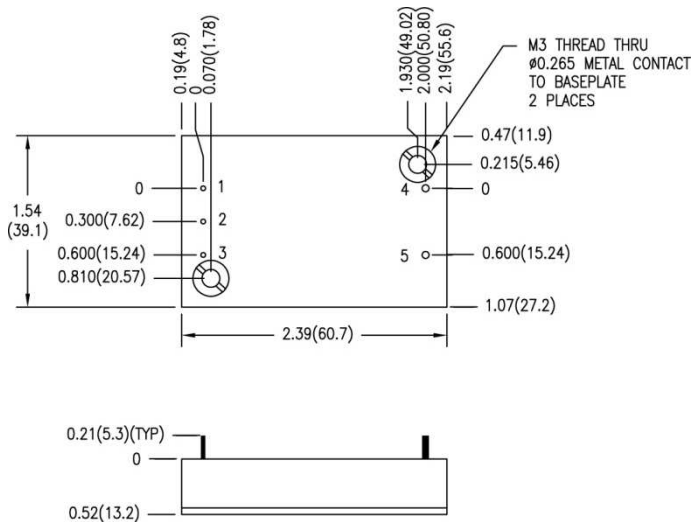
Fig. 13: Input conducted emissions measurement (Typ.) of 24S12.12QSW (ROHS)



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## Mechanical Specification:



### Notes:

Unless otherwise specified:

All dimensions are in inches [millimeters]

Tolerances: x.xx in.  $\pm 0.02$  in [x.x mm  $\pm 0.5$ mm]

x.xxx in.  $\pm 0.010$  in [x.xx mm  $\pm 0.25$ mm]

Torque fasteners into threaded mounting inserts at 10in.lbs. or less. Greater torque may result in damage to unit and void the warranty.

### Input Output Connections:

Pin	Name	Function
1	-INPUT	Negative input voltage
2	ON/OFF	TTL input with internal pull up, referenced to -INPUT, used to turn converter on and off
3	+INPUT	Positive input voltage
4	-OUTPUT	Negative output voltage
5	+OUTPUT	Positive output voltage

### Notes:

1) Pinout is inconsistent between manufacturers of the quarter brick converters. Follow the table above.

