

# DATASHEET

<b>Module TYPE</b>	<b>DDR 512MB/1GB DIMM</b>
<b>Module speed</b>	<b>PC3200</b>
<b>CAS Latency</b>	<b>CL-3</b>
<b>Pin</b>	<b>184pin</b>
<b>SDRAM Operating Temp</b>	<b>0 °C ~ 85 °C</b>

## 1. Features

### Key Parameter

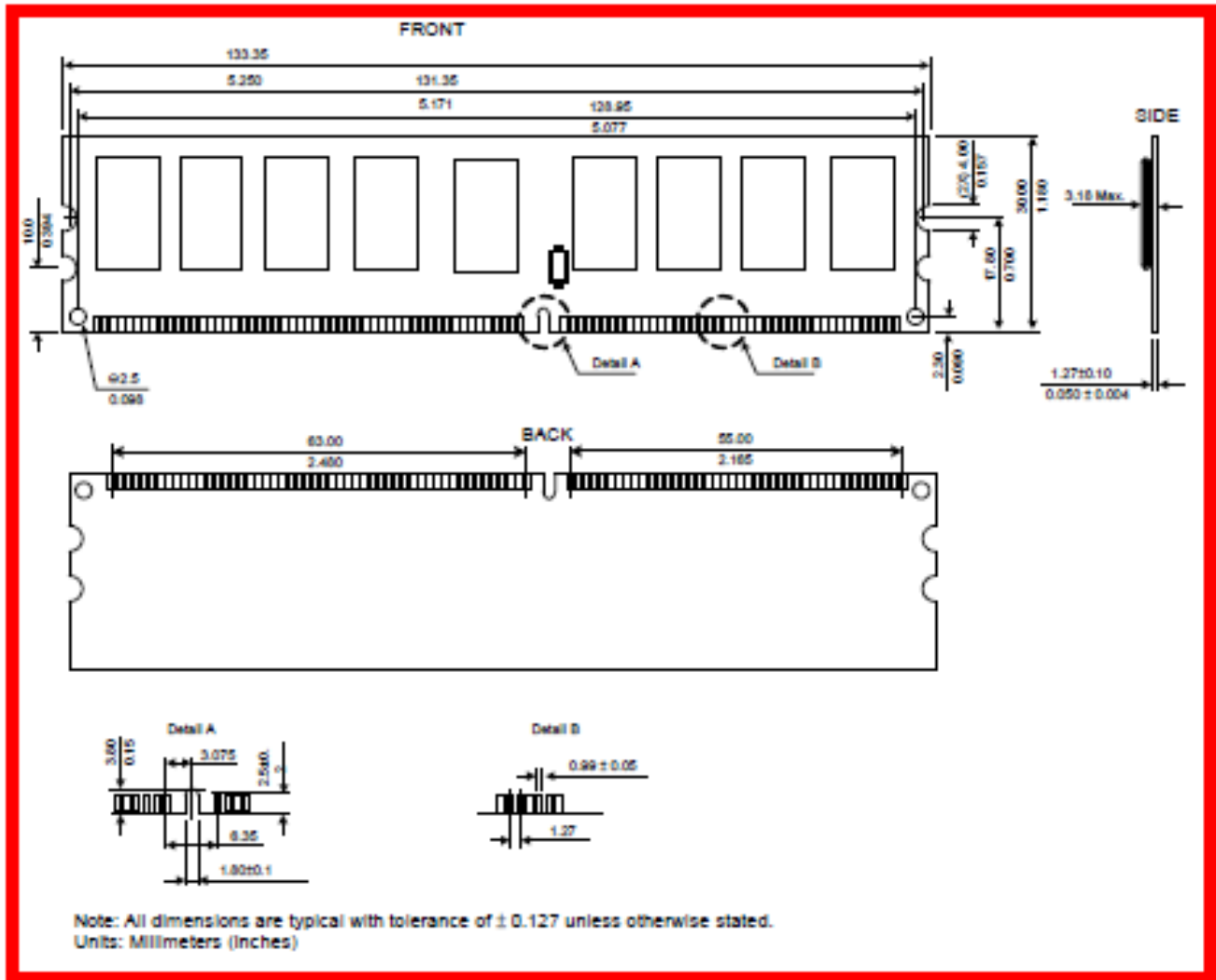
Industry Nomenclature	Speed Grade	Data Rate MT/s			tRCD	tRP	tRC
		CL=2	CL=2.5	CL=3	(ns)	(ns)	(ns)
PC-2600		266	333	400	15	15	55

- JEDEC Standard 184-pin Dual In-Line Memory Module
- Intend for 400MHz applications
- Inputs and Outputs are SSTL-2 compatible
- VDD=VDDQ= 2.6 Volt  $\pm$  0.1 (PC-2600)
- Differential clock input
- DLL aligns DQ and DQS transition with CK transition
- Bi-Directional data strobe with one clock cycle
- Built with 512Mb DDR SDRAMs in 400 mil
- TSOP II packages
- Golden connector (Au:30u")
- ECC function support
- Auto Refresh (CBR) and Self Refresh Modes support.
- Serial Presence Detect with EEPROM
- Auto & self refresh 7.8 $\mu$ s ( $T_A \leq +70^\circ\text{C}$ )
- Operation Temperature
  - $0^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$
  - Commercial ( $0^\circ\text{C} \leq T_A \leq +70^\circ\text{C}$ )
- Programmable Device Operation:
  - Burst Type: Sequential or Inteleave
  - Operation: Burst Read and Write
  - Device CAS# Latency: 2,2.5 and 3
  - Burst Length: 2.4 or 8
- RoHS Compliant (*Section 15*)

Note: 1. The refresh rate is required to double when  $T_c$  exceeds  $85^\circ\text{C}$ .

## 2. PACKAGE DIMENSION

- (512MB, 1 Rank, 64Mx8 DDR SDRAMs)



Note: Device position is only for reference.